

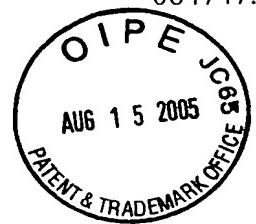
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JRW

ATTORNEY DOCKET  
064747.1016

PATENT APPLICATION  
10/824,873

1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shannon V. Davidson  
Serial No.: 10/824,873  
Filing Date: April 15, 2004  
Title: Graphical User Interface for Managing HPC Clusters

**Mail Stop: Amendments**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
Dear Sir:

CERTIFICATE OF MAILING  
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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

*Willie Jiles*

Willie Jiles

Date: August 15, 2005

**INFORMATION DISCLOSURE STATEMENT**

Applicant respectfully requests, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Copies of all references are enclosed for the convenience of the Examiner. No representation is made that a search has been made, that these references are material to the patentability, or that these references qualify as prior art.

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PATENT APPLICATION  
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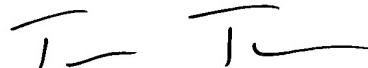
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REMARKS

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement, and this Information Disclosure Statement is submitted before a first Office Action on the merits. Under 37 C.F.R. § 1.97(b)(3), no fee is due. Nonetheless, the Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P.  
Attorneys for Applicant

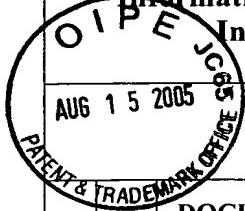


Travis W. Thomas  
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Date: August 15, 2005

Correspondence Address:

**Customer Number 45507**

<b>PTO-1449</b> <b>Information Disclosure Citation</b> <b>In an Application</b>  <b>AUG 15 2005</b>	<b>Application No.</b>		<b>Applicant(s)</b>
	<b>10/824,873</b>		<b>Shannon V. Davidson</b>
	<b>Docket Number</b>	<b>Group Art Unit</b>	<b>Filing Date</b>
	<b>064747.1016</b>		<b>April 15, 2004</b>

**U.S. PATENT DOCUMENTS**

	<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
	A					
	B					
	C					
	D					

**FOREIGN PATENT DOCUMENTS**

	<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>TRANSLATION</b>
						<b>YES</b> <b>NO</b>

**NON-PATENT DOCUMENTS**

	<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>	<b>DATE</b>
E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012031, 6 pages, 08/01/2005 and Written Opinion of the International Search Authority, 5 pages	08/01/2005
F	Chang et al., "Performance Improvement of Allocation Schemes for Mesh-Connected Computers," Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, no. 1, July 10, 1998, 30 pages	07/10/1998
G	Wanqian, et al. "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers," Supercomputing '94, Proceedings, Washington, D.C., November 14-18, 1994, 10 pages	11/14/1994 - 11/18/1994
H	Krevat et al., "Job Scheduling for the BlueGene/L System," Lecture Notes in Computer Science, vol. 2537, July 24, 2002, 18 pages	07/24/2002
I		
J		

<b>EXAMINER</b>	<b>DATE CONSIDERED</b>

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.